



Series: CFW-09

Software: Ve4.15

Language: English

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ADDENDUM TO THE CFW09 FREQUENCY INVERTER

1 INTRODUCTION

This addendum presents information regarding the special firmware version Ve4.15 for CFW-09 frequency inverters assembled in parallel, with the modifications compared to the standard version V3.76 upon which this version has been developed. This document must be used together with the inverter manual.

The Ve4.15 has been developed with the purpose of calculating the initialization current offset only after a 3 second delay. This is necessary because in parallel CFW-09 applications with the PWB VDC3 board, which is connected to the master inverter, the current offset calculation was being performed while only the master inverter DPS3 board was energized, with the slave inverter boards de-energized.

2 OPERATION

A 3 second delay occurs before the current offset calculation when the inverter is energized / initialized.



NOTE!

During this delay the HMI indicates undervoltage "SUB".